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L1	2	((disabli\$4 or deactivat\$4 or inactivat\$4)with (PLL or (phas\$2 near3 loop)))same ((oscillator or reference)near2 (clock or signal))same (power near2 (state or mode))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:55
L2	0	"713"/\$ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:49
L3	23234	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:56
L4	2	I3 and (((disabli\$4 or deactivat\$4 or inactivat\$4)with (PLL or (phas\$2 near3 loop)))same ((oscillator or reference)near2 (clock or signal))same (power near2 (state or mode)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:50
L5	2	I3 and (((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))same ((oscillator or reference)near2 (clock or signal))same (power near2 (state or mode)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:56
L6	4	"6014751".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:54
L7	74640	"365"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:09
L8	0	I7 and (((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))same ((oscillator or reference)near2 (clock or signal))same (power near2 (state or mode)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:56

L9	0	(((disabl\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))same ((selecti\$4 or adapt\$5)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:47
L10	1	(((disabl\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))same ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 10:58
L11	2	I3 and (((disabl\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:00
L12	0	I7 and (((disabl\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:09
L13	6	(((disabl\$4 or deactivat\$4 or inactivat\$4 or depower\$4 or decoupl\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4 coupl\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:01
L14	6	(((disabl\$4 or deactivat\$4 or inactivat\$4 or depower\$4 or decoupl\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4 or coupl\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:01

L15	70900	"345"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:13
L16	1	l15 and (((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:13
L17	62329	"375"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:13
L18	1	l17 and (((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:15
L19	43852	"331"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:14
L20	0	l19 and (((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))and ((selecti\$4 or adapt\$5 or provid\$4 or enabl\$4 or driv\$4)with ((oscillator or reference)near2 (clock or signal)))same (power near2 (state or mode))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 11:14
L21	10	"5822387".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:43

L22	3794	((switch\$4 or chang\$4 or selecti\$4 or alter\$5)with ((oscillator or reference)near2 (clock or signal)))same (PLL or (phas\$2 near2 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:57
L23	2530	((switch\$4 or chang\$4 or selecti\$4 or alter\$5)with ((oscillator or reference)near2 (clock or signal)))with (PLL or (phas\$2 near2 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:46
L24	21	I22 same (power near2 (mode or state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:47
L25	3	I24 and ((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:48
L26	103	I22 and ((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4)with (PLL or (phas\$2 near3 loop)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:59
L27	686	I22 and ((provid\$4 or activat\$4 or enabl\$4 or driv\$4)with (oscillator near3 (signal or clock)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:49
L28	33	I26 and ((provid\$4 or activat\$4 or enabl\$4 or driv\$4)with (oscillator near3 (signal or clock)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:50
L29	225	(selecti\$4 or adapt\$5 or dynamic\$5)near4((switch\$4 or chang\$4 or selecti\$4 or alter\$5 or driv\$4 or output\$4 or coupl\$4)with ((oscillator or reference)near2 (clock or signal)))same (PLL or (phas\$2 near2 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 13:58

L30	0	I29 same ((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4 or decoupl\$4)with (PLL or (phas\$2 near3 loop)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:00
L31	4	I29 and ((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4 or decoupl\$4)with (PLL or (phas\$2 near3 loop)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:24
L32	49	"5142247".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:34
L33	15	((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4 or decoupl\$4)near3 (PLL or (phas\$2 near3 loop)))same ((reduc\$5 or lower\$4 or minimiz\$5)near3 power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:27
L34	9	((disabli\$4 or deactivat\$4 or inactivat\$4 or depower\$4 or decoupl\$4)near3 (PLL or (phas\$2 near3 loop)))with ((reduc\$5 or lower\$4 or minimiz\$5)near3 power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:26
L35	2	"4521918".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:36
L36	25	"4521918".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:34
L37	2	"6691215".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:37

L38	0	"6691215".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/12 14:37
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Lukac, R.; Plataniotis, K.N.; Hatzinakos, D.; Aleksic, M.;
Consumer Electronics, IEEE Transactions on
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Digital Object Identifier 10.1109/TCE.2004.1277871
[AbstractPlus](#) | Full Text: [PDF](#)(1012 KB) IEEE JNL
- ☐ **2. Coding for the blackwell channel: a survey propagation approach**
Wei Yu; Aleksic, M.;
Information Theory, 2005. ISIT 2005. Proceedings. International Symposium on
4-9 Sept. 2005 Page(s):1583 - 1587
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- ☐ **3. A low power symmetrically pulsed dual edge-triggered flip-flop**
Nedovic, N.; Walker, W.W.; Oklobdzija, V.G.; Aleksic, M.;
Solid-State Circuits Conference, 2002. ESSCIRC 2002. Proceedings of the 28th European
24-26 Sept. 2002 Page(s):399 - 402
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- ☐ **4. Subsampling-based image compression prediction for memory-limited applications**
Aleksic, M.; Kouramanis, I.;
Electrical and Computer Engineering, 2004. Canadian Conference on
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- ☐ **5. Improving performance of a DSM system by the communication controller optimizations**
Toncev, M.; Tomasevic, M.; Djordjevi, J.; Aleksic, M.;
Electrical and Computer Engineering, 2004. Canadian Conference on
Volume 2, 2-5 May 2004 Page(s):1035 - 1038 Vol.2
[AbstractPlus](#) | Full Text: [PDF](#)(386 KB) IEEE CNF
- ☐ **6. An improved on-line monitoring technique for a fault-tolerant computing node**
Elahresh, M.; Djordjevic, J.; Tomasevic, W.; Aleksic, M.;
Electrical and Computer Engineering, 2004. Canadian Conference on
Volume 3, 2-5 May 2004 Page(s):1553 - 1556 Vol.3
[AbstractPlus](#) | Full Text: [PDF](#)(463 KB) IEEE CNF
- ☐ **7. Local correlation based CFA Interpolation**
Aleksic, M.; Lukac, R.;
Electrical and Computer Engineering, 2004. Canadian Conference on
Volume 2, 2-5 May 2004 Page(s):793 - 796 Vol.2

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8. Conditional pre-charge techniques for power-efficient dual-edge clocking
 Nedovic, N.; Aleksic, M.; Oklobdzija, V.G.;
 Low Power Electronics and Design, 2002. ISLPED '02. Proceedings of the 2002 International Symposium on
 2002 Page(s):56 - 59
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9. Comparative analysis of double-edge versus single-edge triggered clocked storage elements
 Nedovic, N.; Aleksic, M.; Oklobdzija, V.G.;
 Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
 Volume 5, 26-29 May 2002 Page(s):V-105 - V-108 vol.5
 Digital Object Identifier 10.1109/ISCAS.2002.1010651
[AbstractPlus](#) | Full Text: [PDF\(432 KB\)](#) IEEE CNF
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10. A bridge for a multiprocessor graphics system
 Balatsos, A.; Aleksic, M.;
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 Volume 2, 12-15 May 2002 Page(s):646 - 650 vol.2
 Digital Object Identifier 10.1109/CCECE.2002.1013071
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11. Performance prediction on graphics hardware using software simulation
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 Electrical and Computer Engineering, 2001. Canadian Conference on
 Volume 2, 13-16 May 2001 Page(s):1235 - 1240 vol.2
 Digital Object Identifier 10.1109/CCECE.2001.933618
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12. The impact of out-of-order message delivery on cache coherence protocols
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 Volume 1, 13-16 May 2001 Page(s):399 - 404 vol.1
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13. Conditional techniques for low power consumption flip-flops
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 Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International Conference on
 Volume 2, 2-5 Sept. 2001 Page(s):803 - 806 vol.2
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14. Research and development in e-business on the Internet
 Milutinovic, V.; Cvetkovic, D.; Aleksic, M.; Davidovic, G.; Dimitrijevic, Z.; Dingarac, D.; Grubic, M.; Grujic, A.; Horvat, D.; Horvat, Z.; Ikodinovic, I.; Illic, J.; Illic, N.; Jovanov, E.; Jovanovic, M.; Jovanovic, T.; Knezevic, P.; Krsmanovic, D.; Marinkovic, G.; Mihanovic, M.; Milenkovic, A.; Milicev, D.; Milicevic, M.; Milijakovic, V.; Mulutinovic, D.; Mirkovic, J.; Mitrovic, M.; Nikolic, M.; Petkovic, D.; Petkovic, Z.; Protic, J.; Protic, S.; Raskovic, D.; Savic, S.; Slijepcevic, S.; Sokic, I.; Stefanovic, M.; Tartalja, I.; Tomasevic, M.; Tomca, N.; Radunovic, B.; Vuletic, M.; Zgonjanin, S.;
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 Jan 3-6 2001 Page(s):10 pp.
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15. Timing characterization of dual-edge triggered flip-flops
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 23-26 Sept. 2001 Page(s):538 - 541
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**16. Architecture support for window environments**

Aleksic, M.; Milutinovic, V.;

System Sciences, 1992. Proceedings of the Twenty-Fifth Hawaii International Conference on

Volume i, 7-10 Jan. 1992 Page(s):506 - 514 vol.1

Digital Object Identifier 10.1109/HICSS.1992.183200

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